



Bringing The Trust Back Into Cyber Space:

Extending Intel's commitment to Security

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"Security" is a relative, not absolute, term. No product can provide complete security to the user as all security schemes have inherent limitations. However, some products provide more protection than others.

No computer system can provide absolute security under all conditions. Intel® Trusted Execution Technology (Intel® TXT) requires a computer system with Intel® Virtualization Technology, an Intel TXT-enabled processor, chipset, BIOS, Authenticated Code Modules and an Intel TXT-compatible measured launched environment (MLE). The MLE could consist of a virtual machine monitor, an OS or an application. In addition, Intel TXT requires the system to contain a TPM v1.2, as defined by the Trusted Computing Group and specific software for some uses.

The original equipment manufacturer must provide TPM functionality, which requires a TPM-supported BIOS. TPM functionality must be initialized and may not be available in all countries.

Enabling Execute Disable Bit functionality requires a PC with a processor with Execute Disable Bit capability and a supporting operating system. Check with your PC manufacturer on whether your system delivers Execute Disable Bit functionality.

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More Users, More Devices, More Data

*1 Billion
New
Connected
Users
by 2015*

*>10
Billion
Connected
Devices By
2015*

*800 TB/s
Peak IP
Traffic*

*60 Exabytes
Data Stored*

New (Ecosystem = Vulnerabilities = Threats)

Television



Embedded



Auto



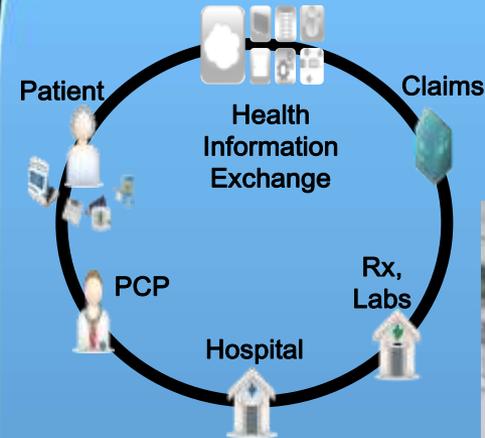
PC Clients



Handhelds



Servers



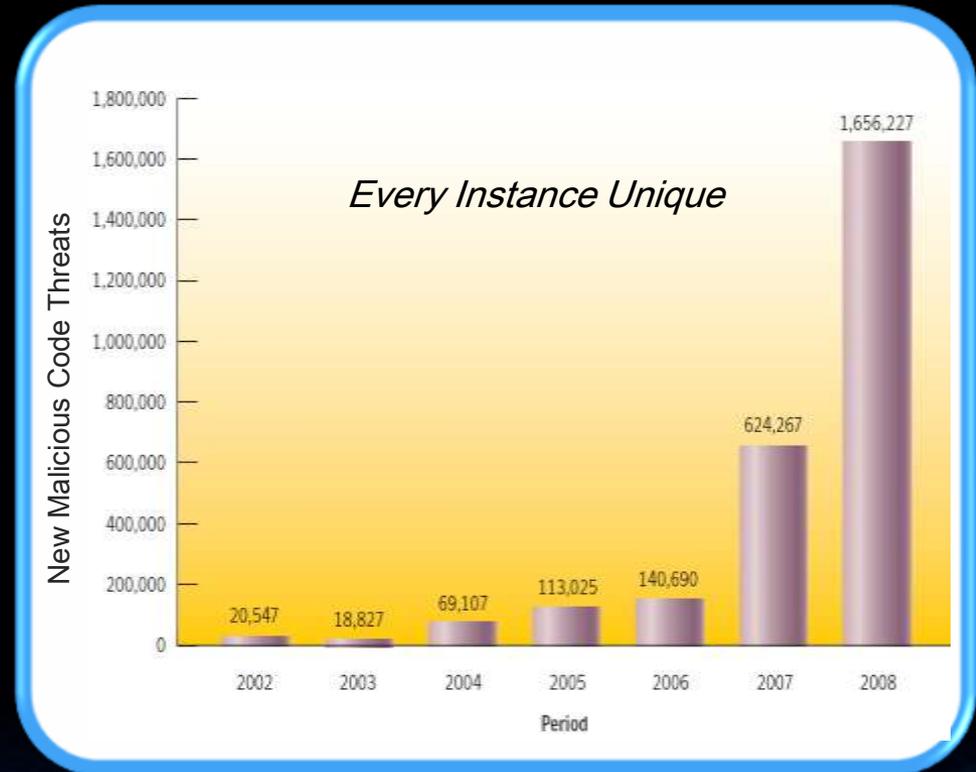
Intel Architecture Across All These Ecosystems

Malware Threats are Outracing Solutions

Goals of Malware have changed –
It is a business today

Asset Protection is Vital

Increasing number of
zero day attacks



No Silver Bullet Solution Today



Areas of Vulnerability

Firmware - A New Target for Attack

“Trusted” third-party Websites
Susceptible to Exploitation Code

Vulnerabilities in Popular
Applications

Buffer Overflows Due to Programming Errors

Fragmented Security Solutions

Cyber Attacks - It's more than Worms, Hacking, & Phishing



Cyber Attacks Are
Holistic, Coordinated, & Smarter

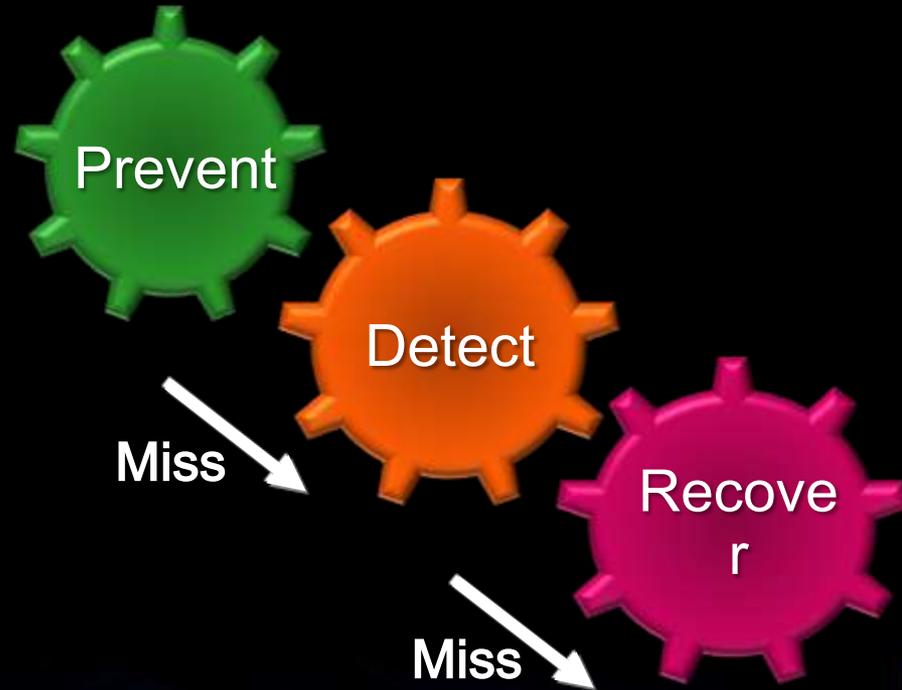


More Unknown Than Known Out There Predict – But Learn To Recover

Prevent the Security & Trust Infractions

Detect the Security & Trust Infractions

Recover *quickly and gracefully* from the Security & Trust Infractions



Intel's Security Vision

Deliver the Foundation for Trustworthy Platforms

Trust
Establishment



Detection &
Protection from
Malware

Asset & Data
Protection



Architectural Leadership



Handheld



Notebook



Desktop



Server

Improving Platform Security
From Handhelds to Data Centers

A “Three Prong Attack” to Combat Malware

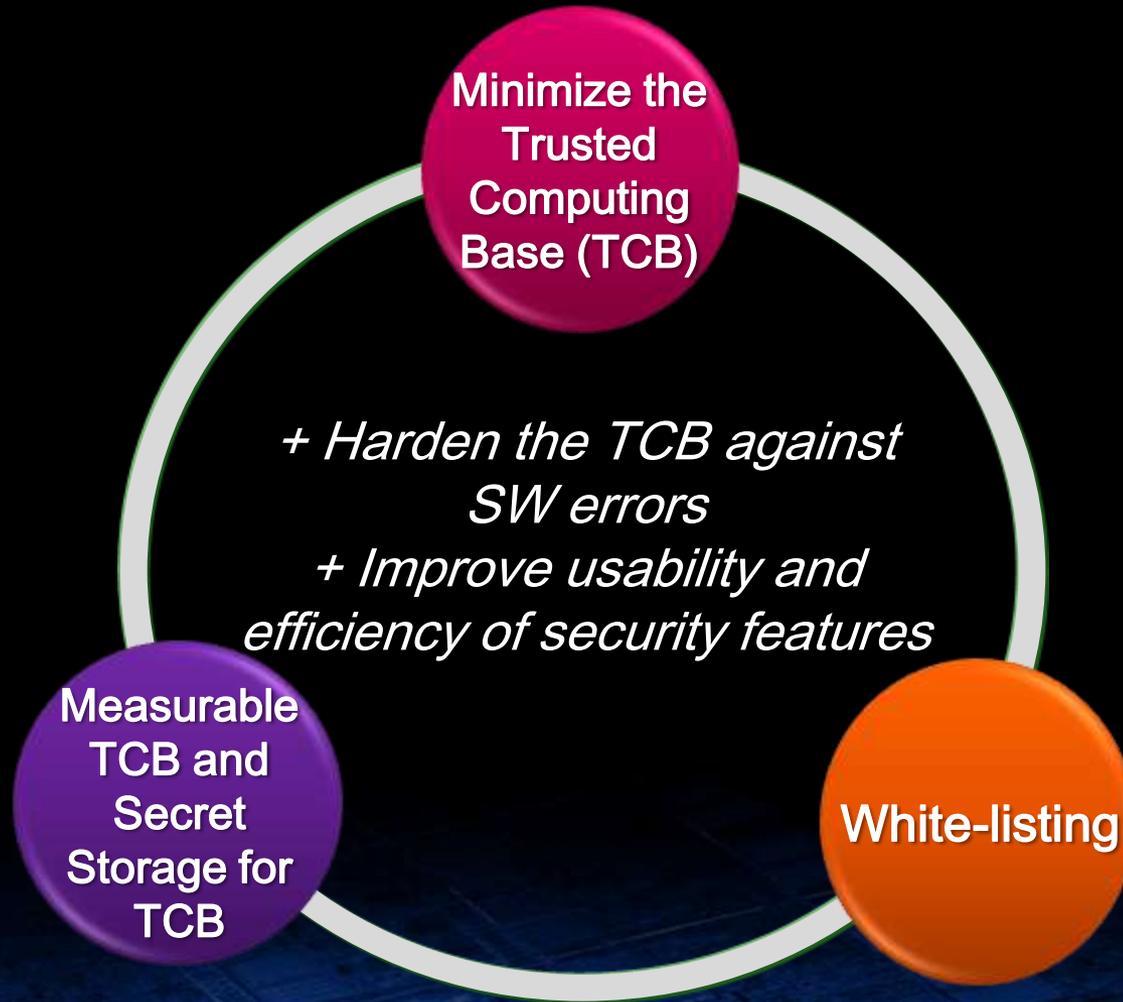
*Continue to **harden** the base architecture*

*Deliver a robust silicon-based **defense***

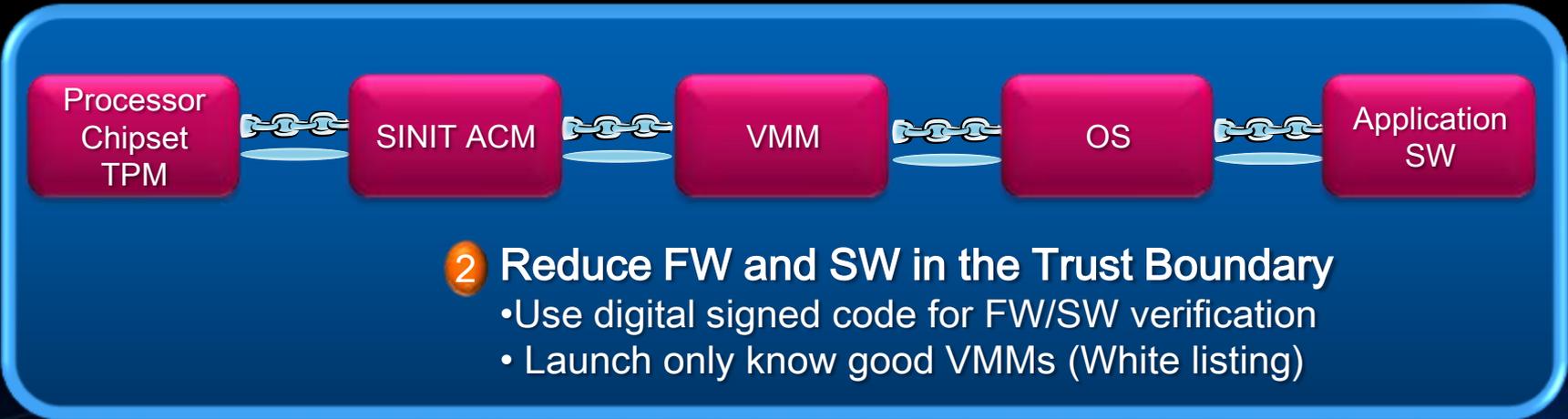
*Deliver a silicon-based **trust solution***



Our Vision on Minimizing the Threat

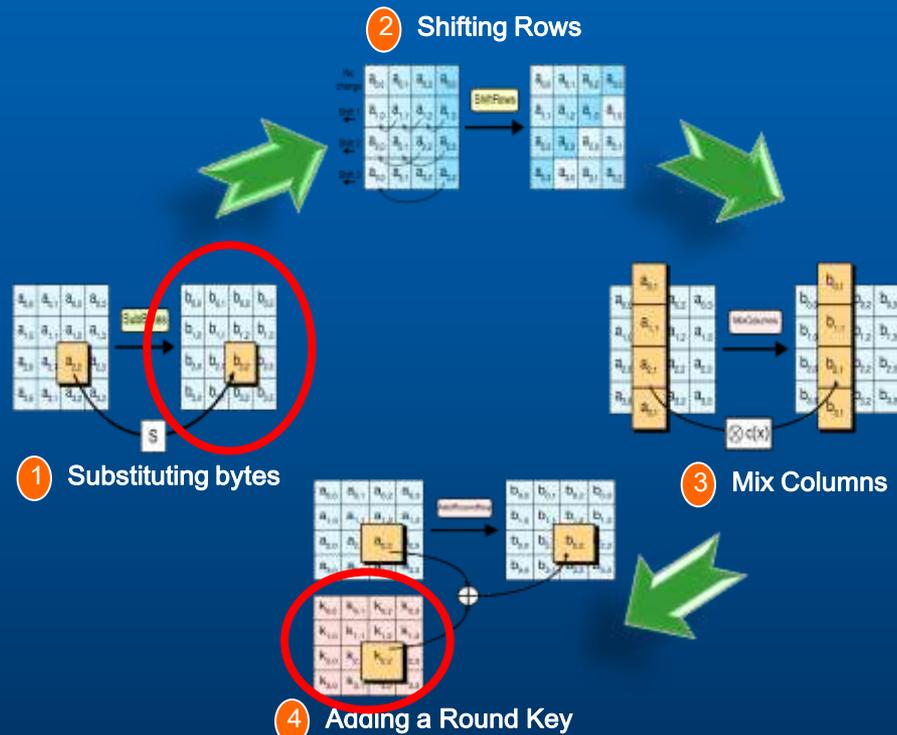


Minimizing the Trusted Computing Base



Securing Against Side Channel Attacks

The AES Operation



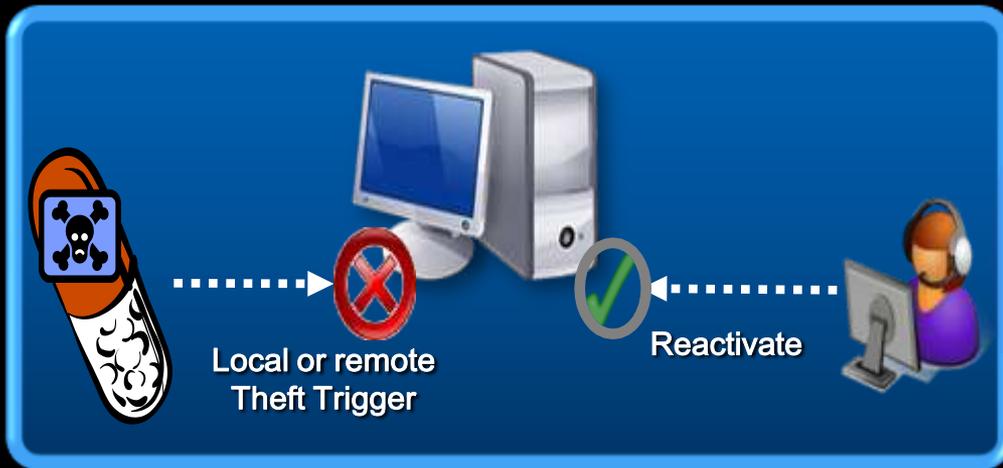
What the attacker wants -
The cipher key

The vulnerability today -
Substitution using look up tables

How the attacker works-
Measure latency of cache lookup

How do we make it more secure -
*Operations done in HW
with fixed latency*

Securing Against Device Theft



Theft Detection

- *Login failures*
- *Timer Expiration*
- *PC hardware / firmware tampering*

"Poison Pill"

- *PC disable*
- *Data access disable*

Recovery

- *Local passphrase / recovery token*
- *Remote unlock*

Looking Over the Horizon

As We Continue Our Focus On Security

Defend
Against
Firmware
Attacks

Keep Data
Secure *and*
Identity
Private

Ensure
Application
Security

Overcome
Programming
Errors

Holistic Security Solution

Across the Entire Hardware-Software Stack

Across All IA Architectures from Embedded to Enterprise

Balance Security
With Usability

Hardware-Software
Co-Design Is Key



Thank You!